FIG.1

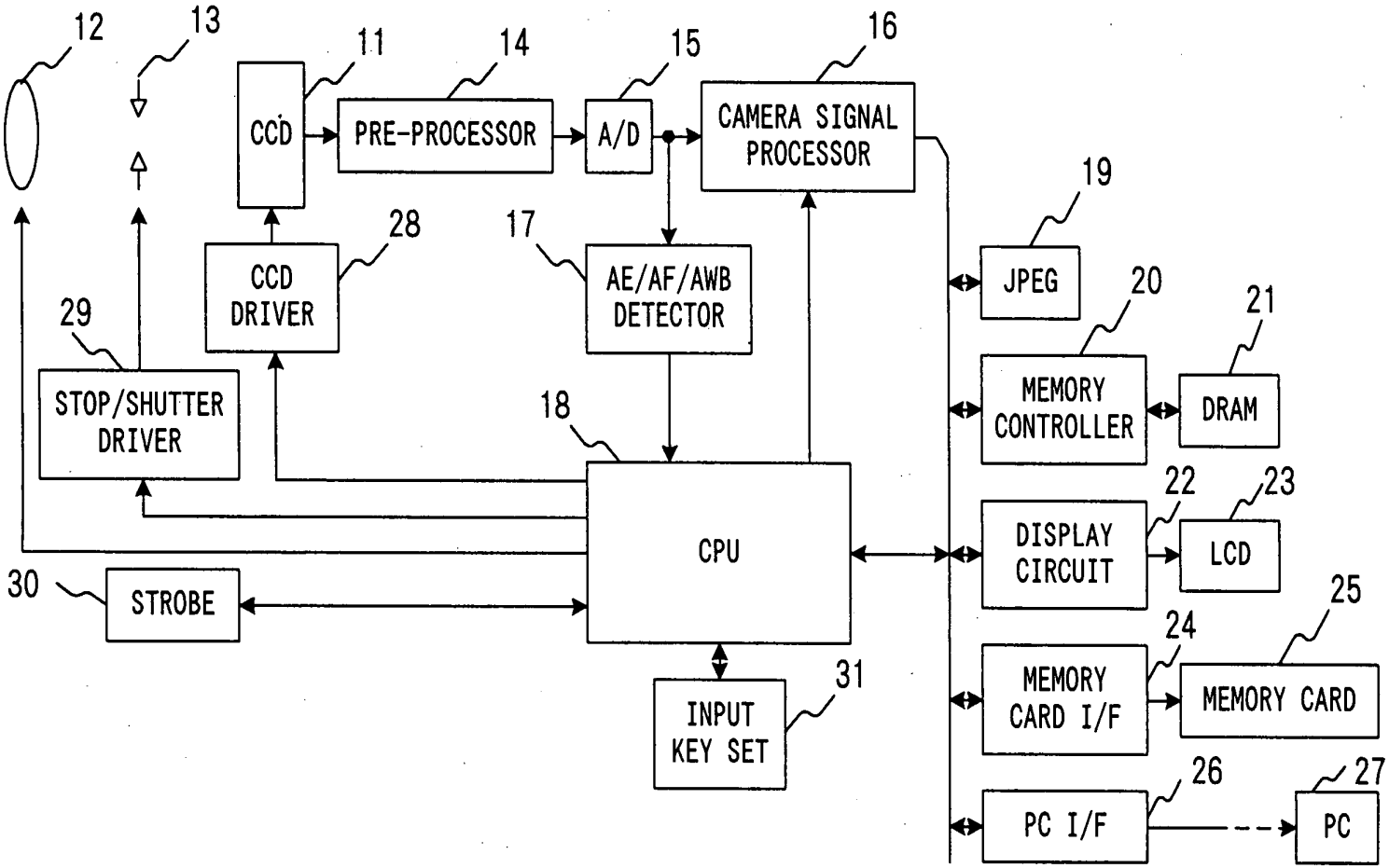

12
13
11
14
15
16
CCD
PRE-PROCESSOR
A/D
CAMERA SIGNAL PROCESSOR
19
28
17
CCD DRIVER
AE/AF/AWB DETECTOR
JPEG
29
20
21
STOP/SHUTTER DRIVER
18
MEMORY CONTROLLER
DRAM
22
23
CPU
DISPLAY CIRCUIT
LCD
30
STROBE
24
25
MEMORY CARD I/F
MEMORY CARD
31
INPUT KEY SET
26
27
PC I/F
PC

FIG.2

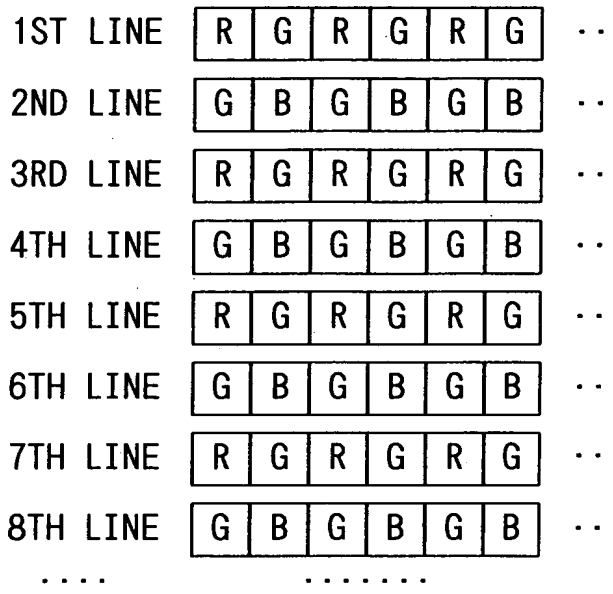

1ST LINE
R G R G R G ..
2ND LINE
G B G B G B ..
3RD LINE
R G R G R G ..
4TH LINE
G B G B G B ..
5TH LINE
R G R G R G ..
6TH LINE
G B G B G B ..
7TH LINE
R G R G R G ..
8TH LINE
G B G B G B ..

FIG.3

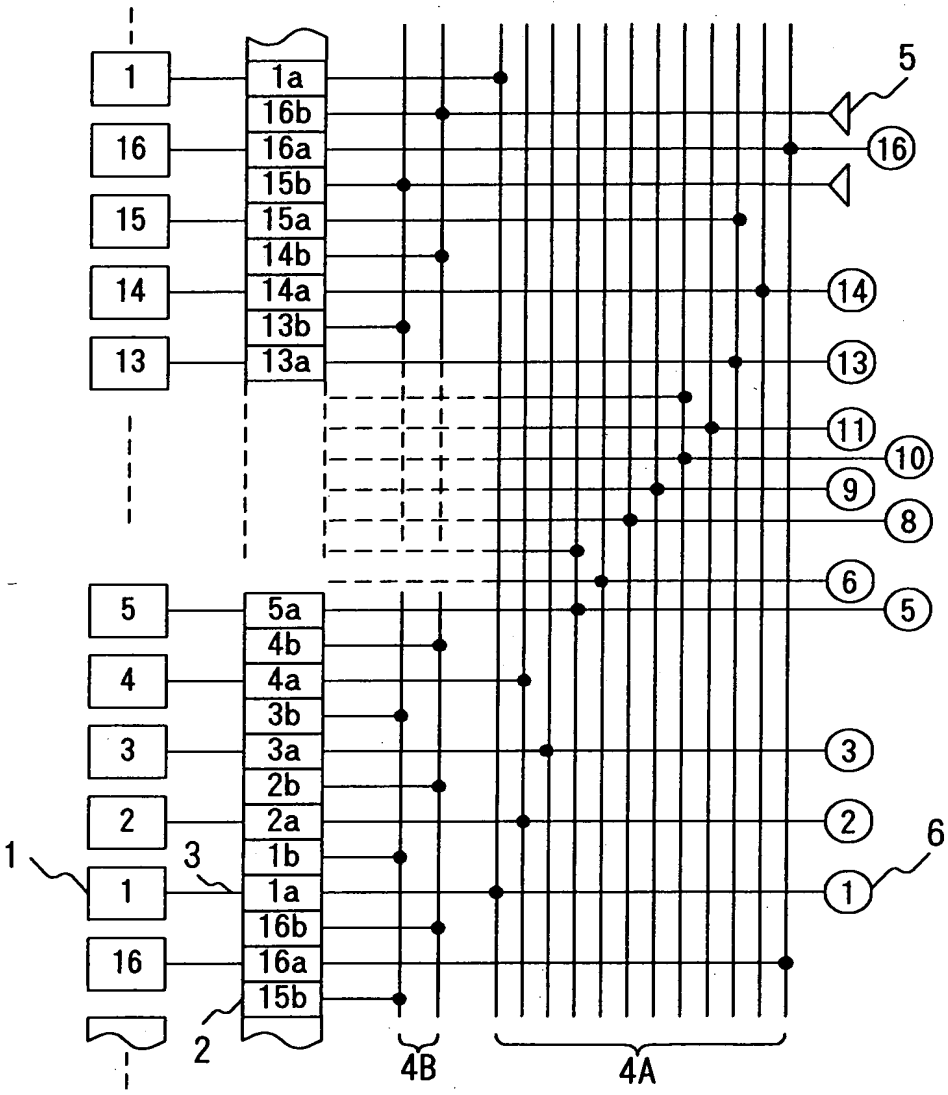

1
16
15
14
13
5
4
3
2
1
16
1a
16b
16a
15b
15a
14b
14a
13b
13a
5a
4b
4a
3b
3a
2b
2a
1b
1a
16b
16a
15b
5
16
14
13
11
10
9
8
6
5
3
2
1
6
1
3
2
4B
4A

FIG.4

READ-OUT MODE	ALLPIXES/ DOUBLE RATE ADDITION				DOUBLE RATE NON-ADDITION				FOUR TIMES RATE								EIGHT TIMES RATE														
									4/16				8/16				2/16		4/16		6/16		8/16								
READ-OUT TIMING→	ODD		EVEN		1ST		2ND		1ST		2ND		1ST		2ND		1ST	2ND	1ST	2ND	1ST	2ND	1ST	2ND							
16	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	16
15	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	15
14	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	14
13	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	13
12	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	12
11	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	11
10	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	10
9	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	9
8	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	8
7	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	7
6	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	6
5	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	5
4	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	4
3	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	3
2	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	G	B	2
1	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	R	G	1

FIG.5

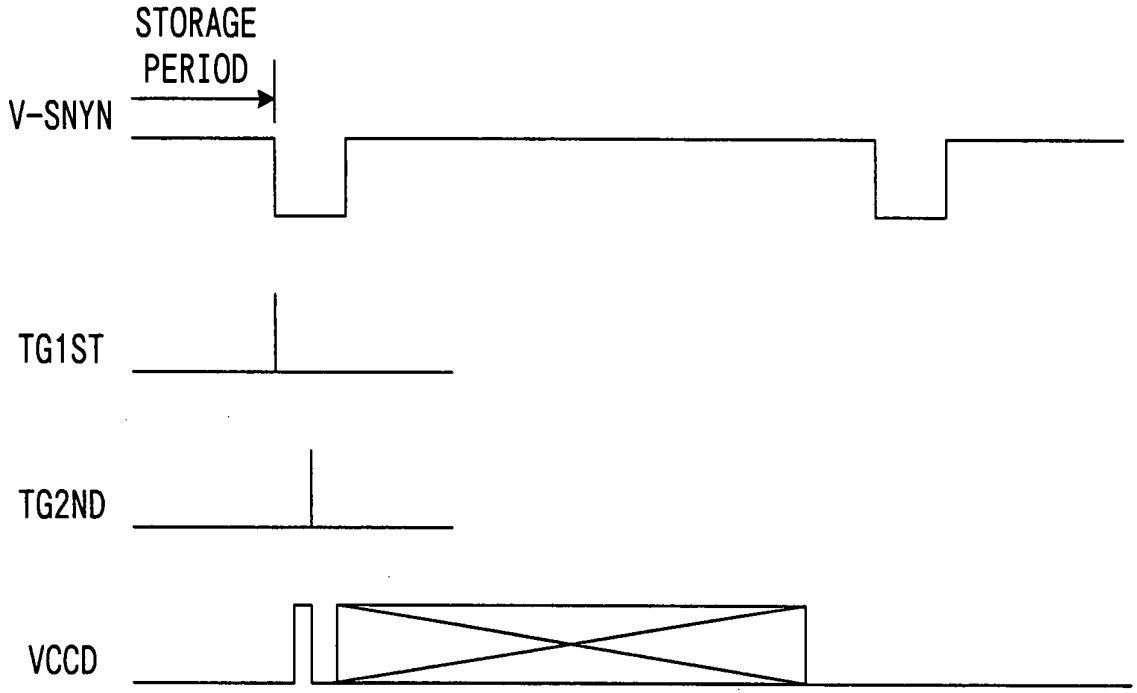


FIG.6

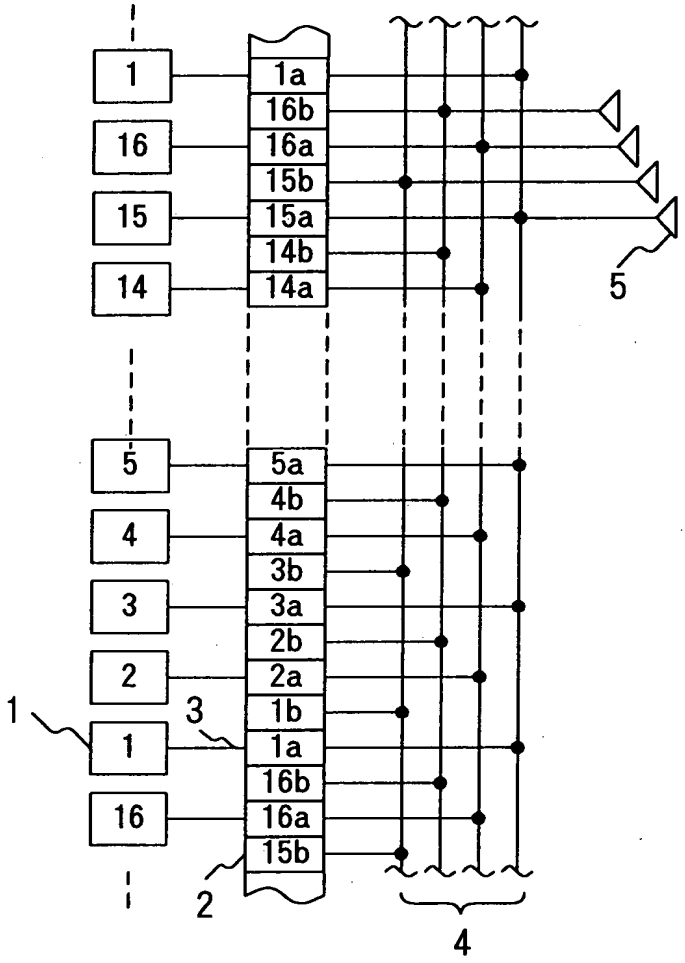
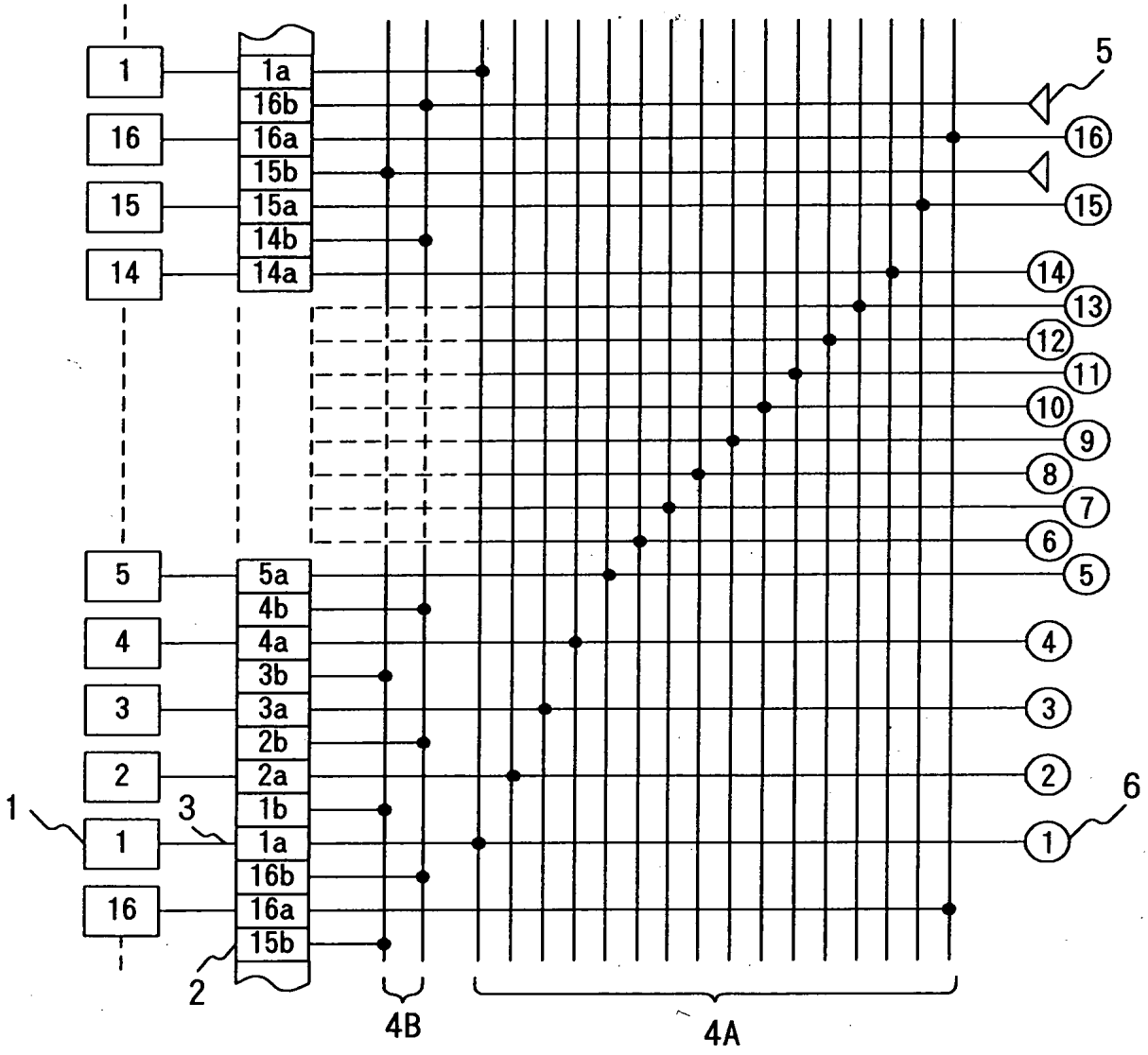

1
16
15
14
5
4
3
2
1
16
1a
16b
16a
15b
15a
14b
14a
5a
4b
4a
3b
3a
2b
2a
1b
1a
16b
16a
15b
1
3
2
5
4

FIG.7


1
16
15
14
5
4
3
2
1
16
1a
16b
16a
15b
15a
14b
14a
5a
4b
4a
3b
3a
2b
2a
1b
1a
16b
16a
15b
1
3
2
5
16
15
14
13
12
11
10
9
8
7
6
5
4
3
2
1
6
4B
4A